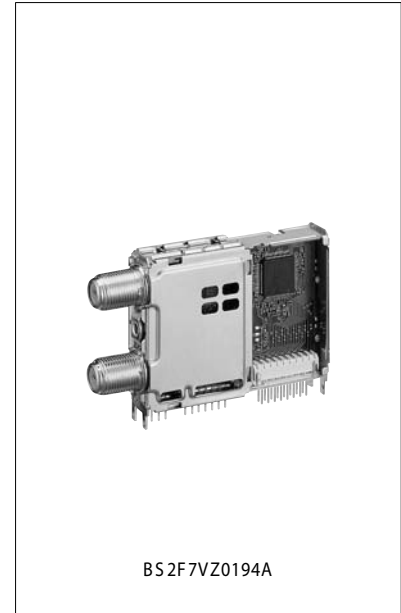


Digital DBS Front-End Units (QPSK Demodulator Circuit Built-in Type)

Specifications <QPSK demodulator circuit built-in type>

Destination	Europe/U.S.A./Japan
Input type	1-input, 1-loop through output
Model No.	BS2F7VZ0194A
Input frequency (MHz)	950 to 2 150
Input signal level (dB m)	-65 to -25
The 1st intermediate frequency (MHz)	Zero-IF (Direct conversion)
Base band frequency bandwidth (MHz)	10 to 30, 2.0 MHz step (BB LPF)
RF input local leak (dB m)	-63 (MAX.)
Output type	8-bit transport
Symbol rate (Mbaud)	45 (MAX.)
BER (Viterbi output)	$E_b/N_o = 5.5$ dB (Max.) [PR = 3/4, BER = 2×10^{-4}]
Channel selection system	PLL (I ² C-bus)*1
Noise figure (dB)	12 (MAX.)
Tuning voltage (V DC)	(Not required)*2
Supply voltage (V DC)	3.3, 2.5
LNB power supply	25 V DC, 400 mA (MAX.)
Input impedance (Ohm)	75
Outline dimensions (mm)	55.1 x 37.9 x 11.0



BS2F7VZ0194A

Contact SHARP for custom design product.

*1 I²C-bus is a trademark of Philips Corporation.

*2 Terminal supply voltage is not required.

Notice

In the absence of confirmation by device specification sheets, SHARP takes no responsibility for any defects that may occur in equipment using any SHARP devices shown in catalogs, data books, etc. Contact SHARP in order to obtain the latest device specification sheets before using any SHARP device.